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... 11 June 1999 9 S.Jarp CERN Initial explanation Lots of details Many questions .proc

getval: alloc r3=ar.pfs,R\_input,R\_local,R\_output,R\_input+R\_local (p0) movl ...

[www.ai.mit.edu/projects/aries/papers/commercial/merced/IA64\\_1.pdf](#) - [Similar pages](#)[gzip-1.3.5.orig/configure +++ gzip-1.3.5/configure @@ -1,6 +1,6 ...](#)

... Copyright: Sverre Jarp (HP Labs ... 4 +#define WSIZE 32768 +#define MAX\_DIST WSIZE -

MAX\_MATCH - MIN\_MATCH - 1 + +#define R\_INPUT 1 +#define R\_LOCAL 31 +#define ...

[debian.ufal.br/debian/pool/main/g/gzip/gzip\\_1.3.5-7.diff](#) - 86k - Supplemental Result - [Cached](#) - [Similar pages](#)

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... r32=r52 r37 r47 outputs dirty dirty 5 10 r37 4 r41 br.call function B B: alloc  
 r32=ar.pfs,5,4 ... <http://www.hp.com/go/ia64> ... Merced Processor & IA-64 Architecture ...  
[www.hpl.hp.com/techreports/1999/HPL-1999-83.pdf](http://www.hpl.hp.com/techreports/1999/HPL-1999-83.pdf) - [Similar pages](#)

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... Other features Capability of understanding IA-64 code Compiler ... questions .proc getval:  
 alloc r3=ar.pfs,R\_input,R\_local ... 1999 12 S.Jarp CERN IA64 Common Registers ...  
[www.ai.mit.edu/projects/aries/papers/commercial/merced/IA64\\_1.pdf](http://www.ai.mit.edu/projects/aries/papers/commercial/merced/IA64_1.pdf) - [Similar pages](#)

**# This is a BitKeeper generated patch for the following project ...**

... storage for fp regs + \*/ + +GLOBAL\_ENTRY(ia64\_load\_scratch\_fpregs) + alloc  
 r3=ar.pfs,1,0,0 ... rp +END(ia64\_load\_scratch\_fpregs) diff -Nru a/include/asm-ia64/pal. ...  
[www.kernel.org/.../v2.4/testing/cset/cset-jsm@udlkern.fc.hp.com%7CChangeSet%7C20020927220903%7C41670.txt](http://www.kernel.org/.../v2.4/testing/cset/cset-jsm@udlkern.fc.hp.com%7CChangeSet%7C20020927220903%7C41670.txt) - 6k -  
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**Mode: outline-\* Light-weight System Calls for IA-64 ...**

... Light-weight System Calls for IA-64 --- Started: 13 ... handler The file  
 arch/ia64/kernel/fsys.S ... r10 = 0 - r11 = saved ar.pfs (a user ...  
[www.ibiblio.org/peanut/Kernel-2.6.6/ia64/fsys.txt](http://www.ibiblio.org/peanut/Kernel-2.6.6/ia64/fsys.txt) - 13k - [Cached](#) - [Similar pages](#)

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... set and Other features Capability of understanding IA-64 code Compiler ... f8=[r2] //  
 Load value (p0) mov ar.pfs=r3 (p0) br ... 1999 11 S.Jarp CERN IA64 Common Registers ...  
[nicewww.cern.ch/~sverre/IA64\\_1.pdf](http://nicewww.cern.ch/~sverre/IA64_1.pdf) - [Similar pages](#)

**Re: SIGILL in \_\_libc\_csu\_fini**

... mov r12=r32 > 0x4000000000002801 <\_init+33>: mov.l ar.pfs=r34 > 0x4000000000002802 ...  
 Thanks > Ian > > - To UNSUBSCRIBE, email to [debian-ia64-REQUEST@lists](mailto:debian-ia64-REQUEST@lists.debian.org) ...  
[lists.debian.org/debian-ia64/2004/04/msg00015.html](http://lists.debian.org/debian-ia64/2004/04/msg00015.html) - 11k - [Cached](#) - [Similar pages](#)  
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**Re: IA-64 port**

... 32 + ldf.fill.nta f27 = [r3],32 + mov ar.pfs = r11 + ;; + ldf ... 10:18:05 2001 @@  
 -0,0 +1,57 @@ +/\* ia64/linux/md.h + \* Linux IA-64 configuration information ...  
[www.mail-archive.com/kaffe@rufus.w3.org/msg02563.html](http://www.mail-archive.com/kaffe@rufus.w3.org/msg02563.html) - 38k - [Cached](#) - [Similar pages](#)  
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**sys/ia64/ia64/db\_interface.c**

... 55 #include <ddb/db\_variables.h> 56 57 #include <ia64/disasm/disasm ... db\_frame}, 77  
 {"rp", DB\_OFFSET(tf\_special.rp), db\_frame}, 78 {"ar.pfs", DB\_OFFSET(tf\_special ...  
[fxr.watson.org/fxr/source/ia64/ia64/db\\_interface.c](http://fxr.watson.org/fxr/source/ia64/ia64/db_interface.c) - 69k - [Cached](#) - [Similar pages](#)

**LKML: Keith Owens: [patch] 2.6.6-rc2 Allow architectures to ...**

... ia64/kernel/head.S Mon Apr 5 11:02:53 2004 +++ 2.6.6-rc2/arch/ia64/kernel/head.S  
 Tue Apr 27 11:52:00 2004 @@ -866,12 +866,14 @@ \* Inputs: \* ar.pfs - saved CFM ...  
[lkml.org/lkml/2004/4/26/221](http://lkml.org/lkml/2004/4/26/221) - 14k - [Cached](#) - [Similar pages](#)

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Architecture Espen Skoglund Page 2. © 2001 Universität ...

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... Linux/IA64 Support for Performance Monitoring – Stéphane. ... dynamically optimizes analysis code to reduce cost of instrumentation (up to ... alloc r34=ar.pfs,6,4,0. ...  
 rogue.colorado.edu/Pin/ CGOTutorialSlides/PinCgoTutorial.ppt - [Similar pages](#)

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... and dynamic optimizers for x86, PA-RISC, and IA64. ... an example, an out-of-line instrumentation strategy can ... for clarity): foo:: #l.caliper alloc r33=ar.pfs,0,11 ...  
 hpcll.usfc.edu.cn/doc/caliper.pdf - [Similar pages](#)

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... pfs pfs,0, ,0, mov mov r11=r12;; r11=r12 ... Caliper does run-time instrumentation of the +O1 executable. ... on HP-UX and Linux – PI, IA64 Performance Investigator. ...  
 h71028.www7.hp.com/enterprise/downloads/ Squeezing\_performance\_Itanium\_100803.pdf - [Similar pages](#)

**diff -X /build2/hawkes/Build/ignore.dirs -Naur linux-2.4.3 ...**

... you can say N. + +mcount() instrumentation +CONFIG\_MCOUNT + This ... linux-2.4.3-profile/  
 arch/ia64/lib/mcount ... h> + +GLOBAL\_ENTRY(\_mcount) + alloc loc0 = ar.pfs, 4, 3 ...  
 ftp.uos.ac.kr/p/development/sgi/kernprof/ download/old/profile-0.9-2.4.3-ia64-010405.patch - 42k - Supplemental Result -  
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**diff -X /build4/hawkes/Build/ignore.dirs -Naur linux-2.4.0-test11 ...**

... you can say N. + +mcount() instrumentation +CONFIG\_MCOUNT + This will ... 2.4.0-test11-  
 profile/arch/ia64/lib/mcount ... GLOBAL\_ENTRY(\_mcount) + alloc loc0 = ar.pfs, 4, 3 ...  
 ftp.uos.ac.kr/p/development/sgi/ kernprof/download/old/profile-0.7-2.4.0-11.patch - 82k - Supplemental Result  
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**WIESS Paper**

... to. // generate free register. alloc r33=ar.pfs,0,12 ... Caliper will be ported to Linux  
 on IA64 processors and ... running PA-RISC applications on IA-64 demonstrates this ...  
 www.usenix.org/events/osdi2000/ wiess2000/full\_papers/hundt/hundt\_html/ - 65k - [Cached](#) - [Similar pages](#)

**David Mosberger - Re: new syscall stub support for ia64 libc**

... ASM\_OUTARGS\_##nr \ + : "2" (\_r15) ASM\_ARGS\_##nr \ + : "memory", "ar.pfs"  
 ASM\_CLOBBERS\_##nr ... 0700 +++ nptl/sysdeps/unix/sysv/linux/ia64/dl-sysdep.h ... IA-64 version. ...  
 sources.redhat.com/ml/libc-hacker/2003-11/msg00000.html - 37k - [Cached](#) - [Similar pages](#)

**HP Caliper: A Framework for Performance Analysis Tools**

... As an example, an out-of-line instrumentation strategy may perform ... case, foo's—would  
 look like this in IA-64 Assembly: foo:: alloc r33=ar.pfs,0,11,1,0 addl r9 ...  
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... slot 1. [4,5] The ar.pfs register is ... Barton P. Miller, "Dynamic Instrumentation API",  
 Journal ... pdf [8] Hewlett-Packard Company, "IA-64 Software Conventions ...  
 h21007.www2.hp.com/dspp/files/ unprotected/Itanium/DSP-PP-DynInstrCPP.pdf - [Similar pages](#)


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... stack frame is local+output, which can be up to 96 registers, specified by an alloc instruction as the following format: alloc <target\_reg>=ar.pfs in,local,out ...

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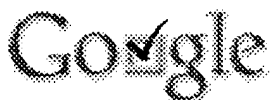
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... The making of Linux/ia64 Stéphane Eranian David Mosberger Hewlett ... call function B B: alloc r32=ar.pfs,5,4 ... spills and fills on function calls, IA-64 provides a ...

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... r32 r52 r37 r47 outputs dirty dirty 5 10 r37 4 r41 br.call function B B: alloc

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... 16+SIGCONTEXT\_OFF),sp - ;; - .spillsp ar.pfs, CFM\_OFF+SIGCONTEXT\_OFF ... the desirable effect of making it possible ... Matt Chapman: "Re: [Linux-ia64] sigaltstack and ...

[www.gelato.unsw.edu.au/linux-ia64/0302/4644.html](http://www.gelato.unsw.edu.au/linux-ia64/0302/4644.html) - 13k - [Cached](#) - [Similar pages](#)**Linux IA64 Archives: [Linux-ia64] Re: new ia64 kernel patch ...**

... missing for INIT and MCA dumps, making it impossible ... Rest - caller's state, do not use, especially ar.pfs. ... lists for spin\_lock() in include/asm-ia64/spinlock.h ...

[www.gelato.unsw.edu.au/linux-ia64/0305/5415.html](http://www.gelato.unsw.edu.au/linux-ia64/0305/5415.html) - 12k - [Cached](#) - [Similar pages](#)[ [More results from www.gelato.unsw.edu.au](#) ]**David Mosberger - patch to fix unwind info for ia64**

... 20:15:59 -0000 1.6 +++ sysdeps/ia64/memccpy.S ... double-check the code-layout after making any changes ... endif + ENTRY(memccpy) .prologue alloc r2 = ar.pfs, 4, 40 ...

[sources.redhat.com/ml/libc-hacker/2003-11/msg00069.html](http://sources.redhat.com/ml/libc-hacker/2003-11/msg00069.html) - 13k - [Cached](#) - [Similar pages](#)**eranian at hpl dot h - [Bug c/17455] New: syscall linkage ...**

... We have found a problem in the Linux/ia64 kernel related to ... Any modification requires making a copy ... follows: 0000000000000000 &lt;bar&gt;: 0: [MII] alloc r33=ar.pfs,4,3 ...

[gcc.gnu.org/ml/gcc-bugs/2004-09/msg01343.html](http://gcc.gnu.org/ml/gcc-bugs/2004-09/msg01343.html) - 8k - [Cached](#) - [Similar pages](#)**Boehm, Hans - Trunk gcj on IA64**

... I'm making it available now, since I won't get ... flow.c, config.ia64/ia64.h: Treat ar.pfs live before ... config/ia64/ia64.c, config/ia64/linux.c: Exception handling ...

[gcc.gnu.org/ml/java/2001-08/msg00103.html](http://gcc.gnu.org/ml/java/2001-08/msg00103.html) - 5k - Oct 31, 2004 - [Cached](#) - [Similar pages](#)[ [More results from gcc.gnu.org](#) ]**[hxcommon] CR: IA64 (Itanium) atomic operators and spinlock ...**

... Interestingly, the IA-64 processor has an assembly instruction ... global

\_HXMutexClearBit \_HXMutexClearBit: alloc loc0 = ar.pfs, 1, 1 ... HP-UX / IA64 (Native compiler ...

[lists.helixcommunity.org/pipermail/common-dev/2003-August/000411.html](http://lists.helixcommunity.org/pipermail/common-dev/2003-August/000411.html) - 23k -[Cached](#) - [Similar pages](#)**PERFORCE change 29149 for review**

... ld8 r18=[r15] // fcsr - mov ar.pfs=r17 - ;; -) mmb ... depot/projects/ia64\_epc/sys/ia64/

<http://www.google.com/search?hl=en&lr=&q=ar.pfs+IA-64+making+of+Linux&btnG=Search>

11/2/04

ia64/vm\_machdep.c ... gr8 = 0; /\* child's pid (linux) \*/ - p2tf->tf\_scratch ...

lists.freebsd.org/pipermail/ p4-projects/2003-April/000403.html - 12k - Supplemental Result -

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... You can only keep making a fool of > yourself by ... net/viewcvs.py/linux-vax/gl >

bc/sysdeps/ia64/memcpy.S ... C code: ENTRY(memcpy) .prologue alloc r2 = ar.pfs, 3,  
16 ...

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thread=562452&start=75&range=15&hlite=false&q= - 54k - Cached - Similar pages

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